

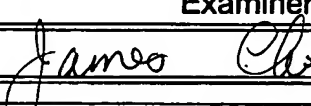

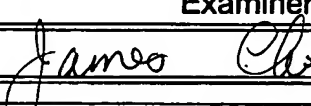

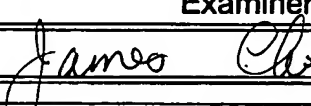


# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

<b>Title of Invention</b>	<b>Half-Rate Clock Logic Block and Method for Forming Same</b>																				
<p>Application Number: 10/623303 </p> <p>Confirmation Number: 3436</p> <p>First Named Applicant: Jeffrey Grundvig</p> <p>Attorney Docket Number: 075903-197</p> <p>Art Unit: <del>2846</del> 2879</p> <p>Search string: ( 5301196 ).pn.</p> <p><b>US Patent Documents</b></p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>5301196</td><td>1994-04-05</td><td>Ewen, et al</td><td></td><td></td><td></td></tr></tbody></table> <p><b>Signature</b></p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td></td><td>10/28/04</td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	5301196	1994-04-05	Ewen, et al				Examiner Name	Date		10/28/04
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